

FIG. 2 (PRIOR ART)

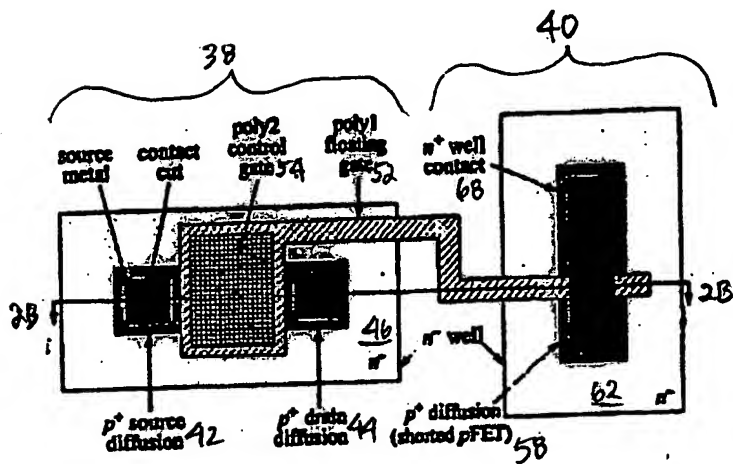


FIG. 3A

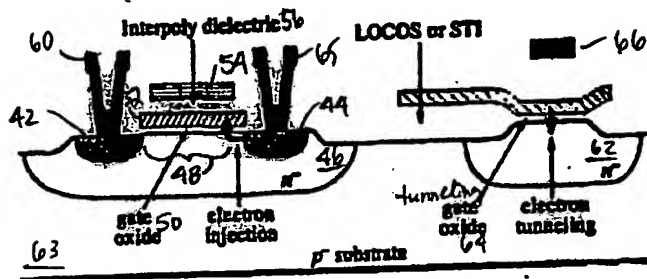


FIG. 3B

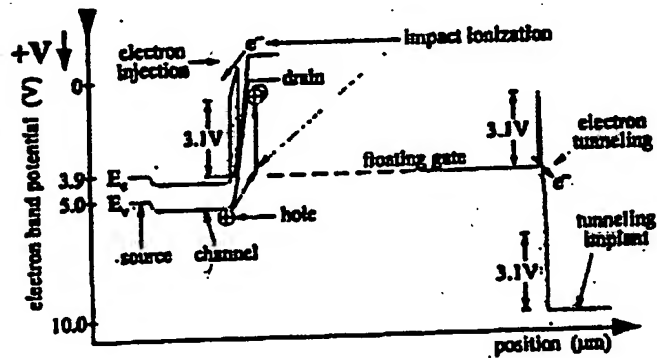


FIG. 3C

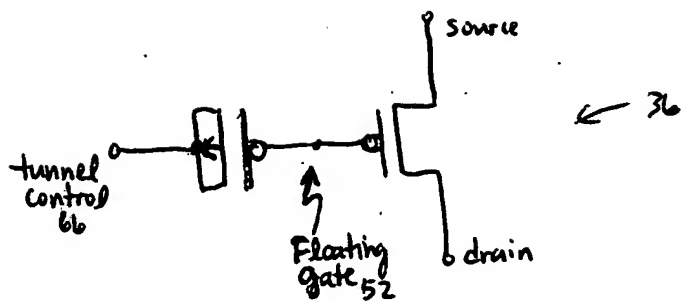


FIG. 3D

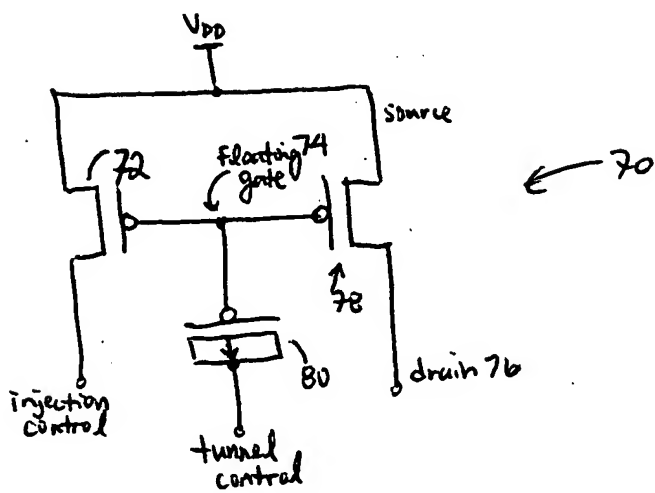


FIG. 3E

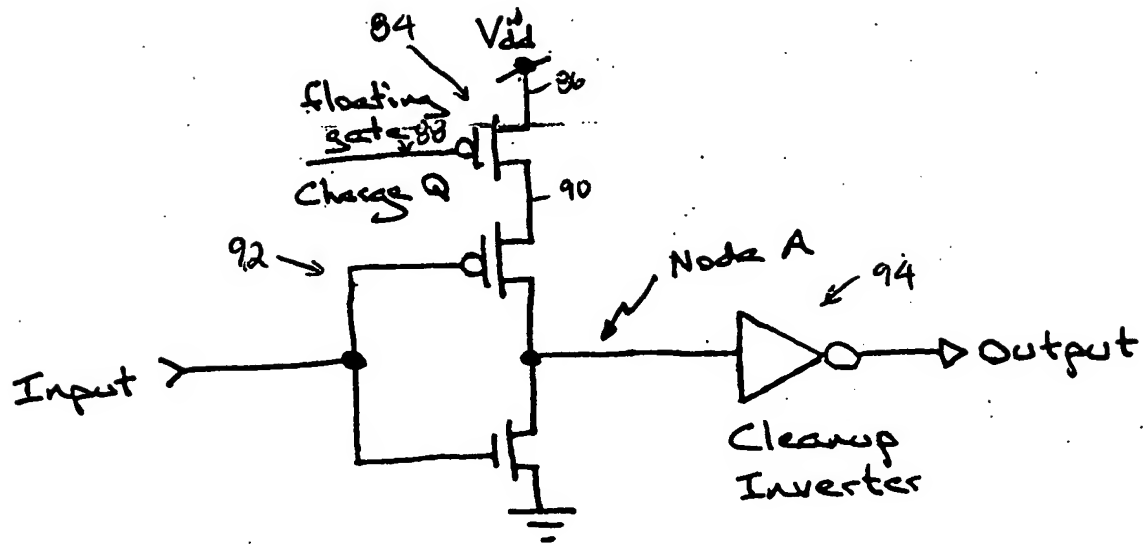


FIG. 4A

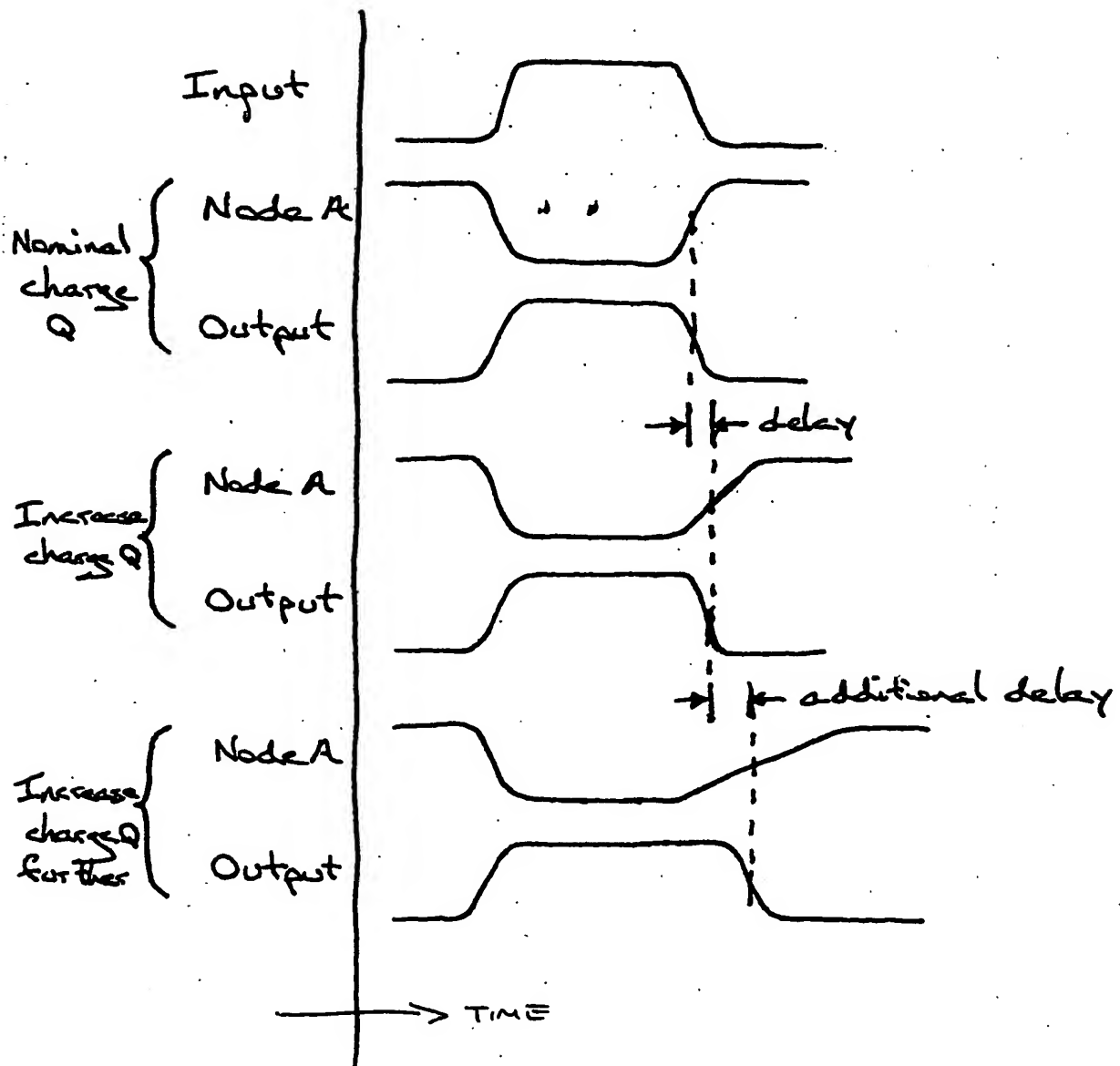


FIG. 4B

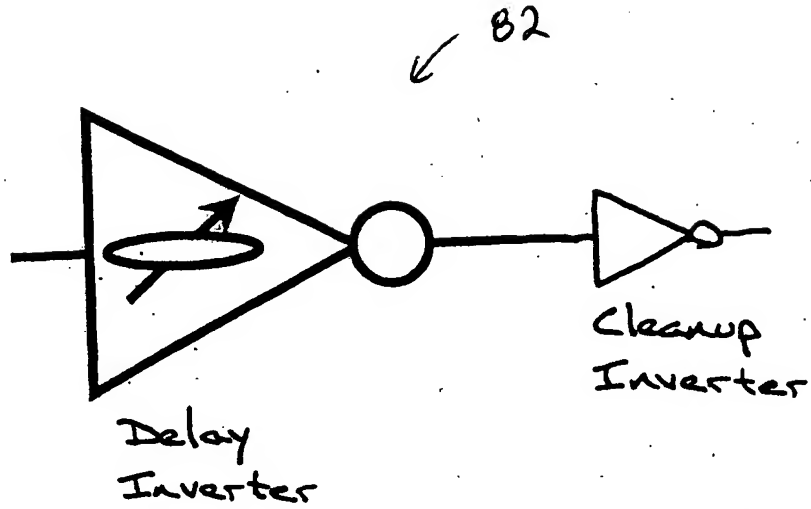


FIG. 4C

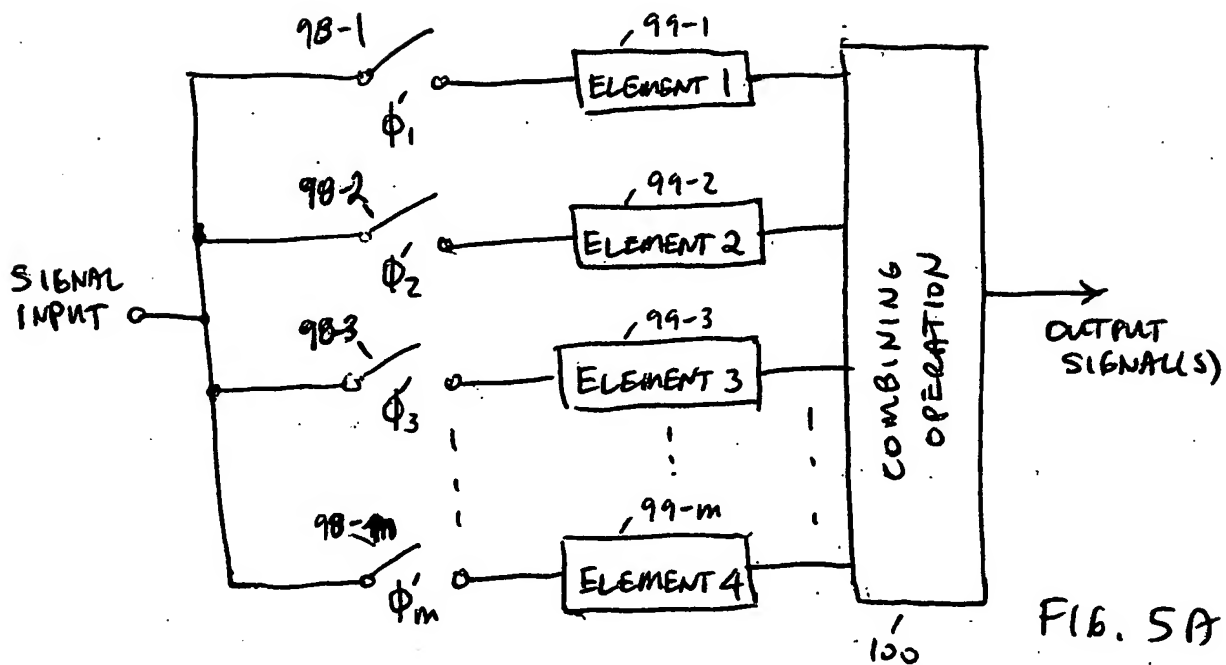


FIG. 5A

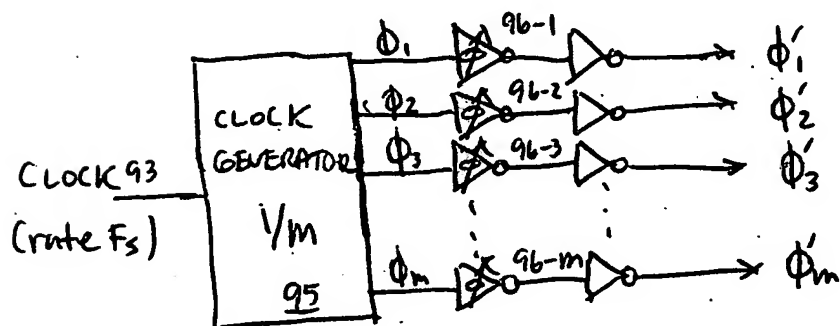


FIG. 5B



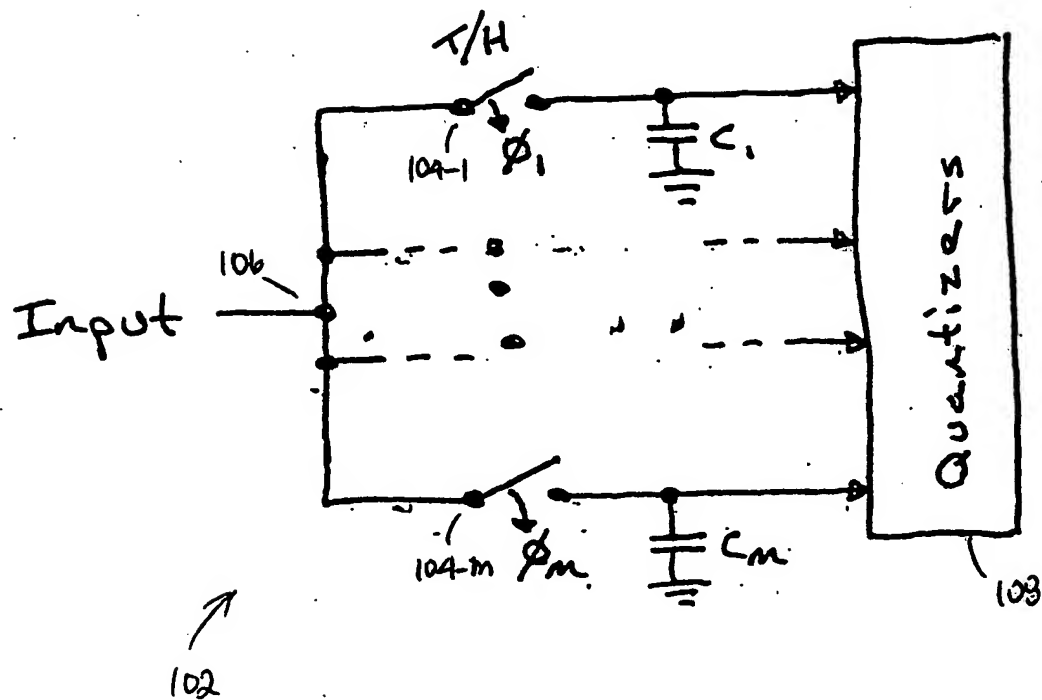


FIG. 6A

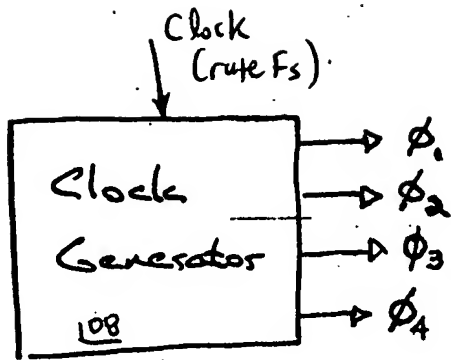


FIG. 6B

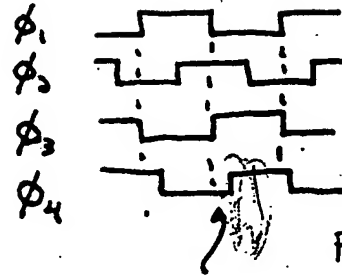
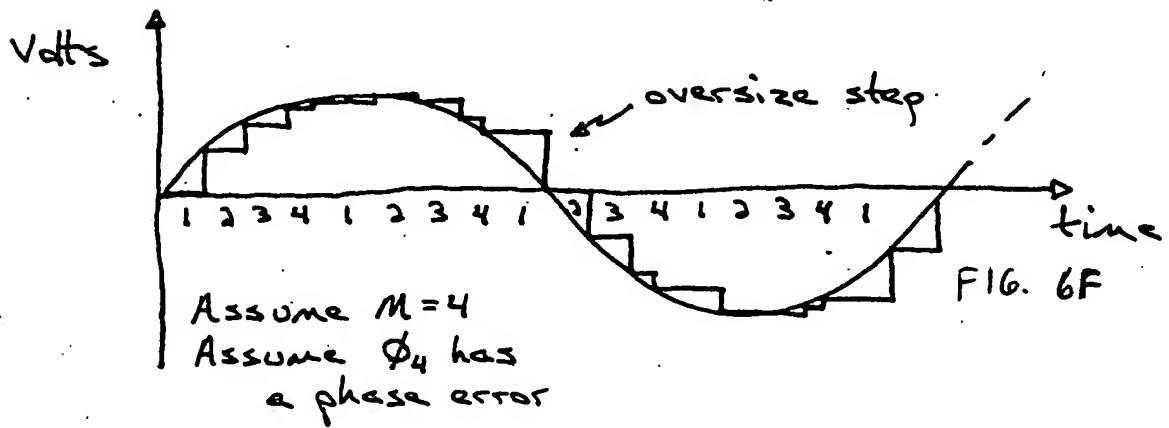
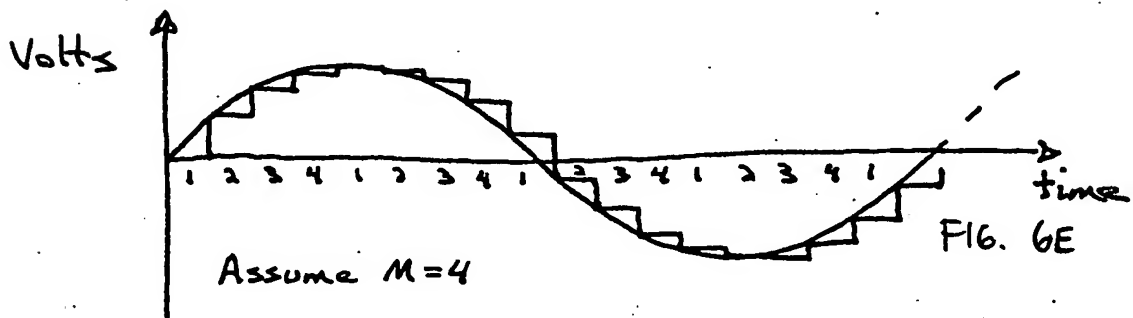
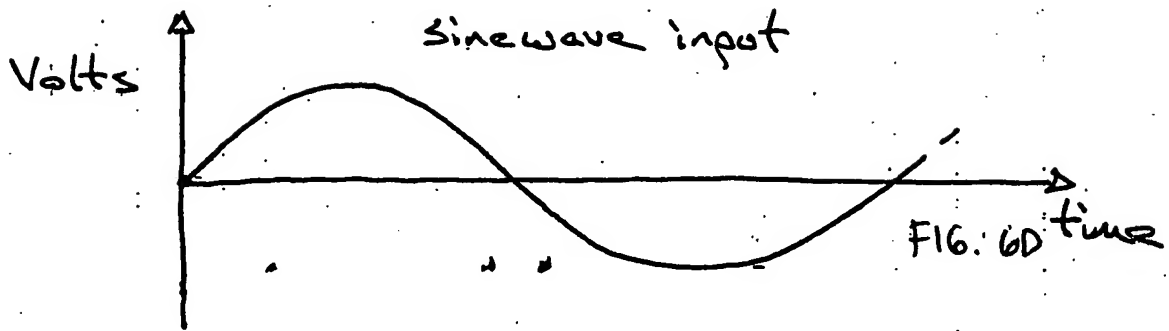


FIG. 6C

phase error  
in  $\phi_4$



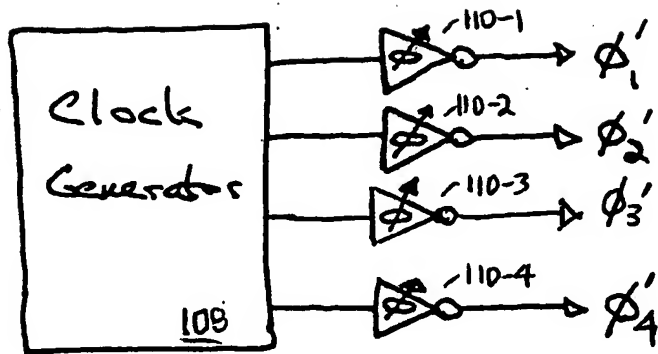


FIG. 7A

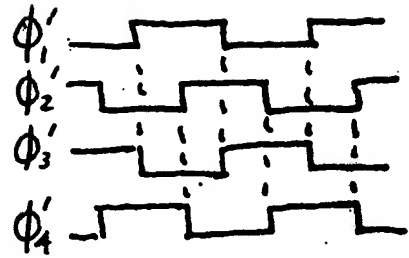


FIG. 7B

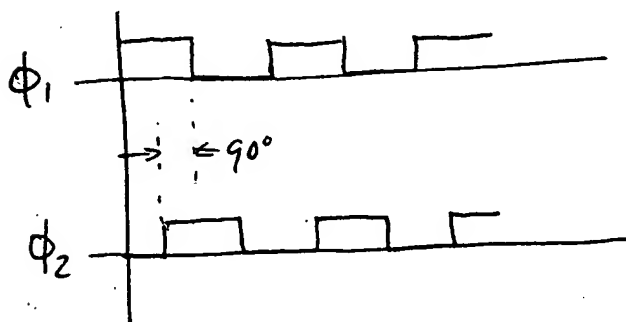
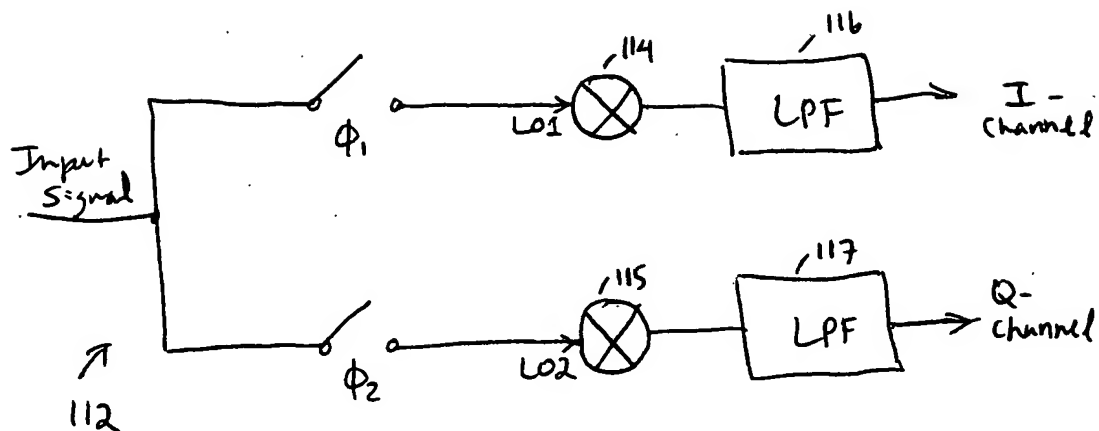


FIGURE 8A

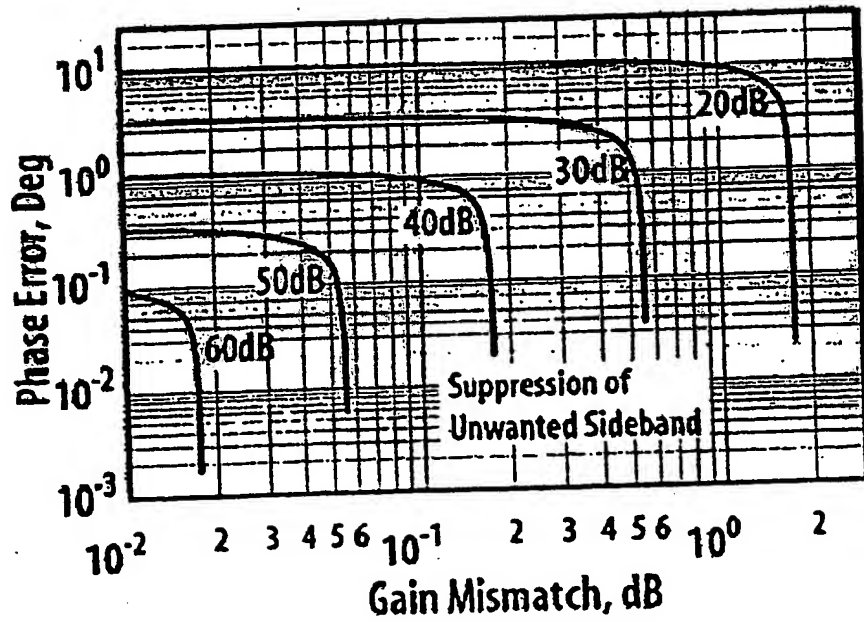


FIG. 8B

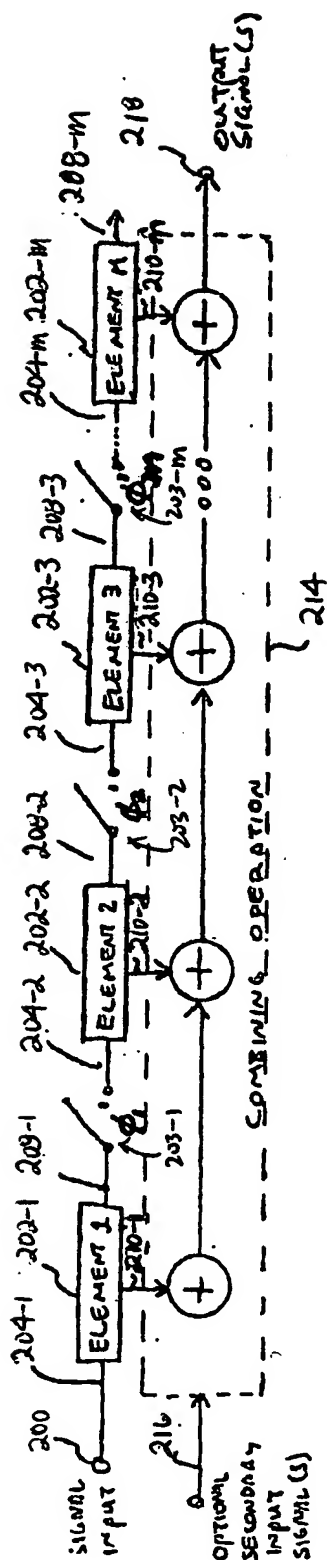


FIG. 9A

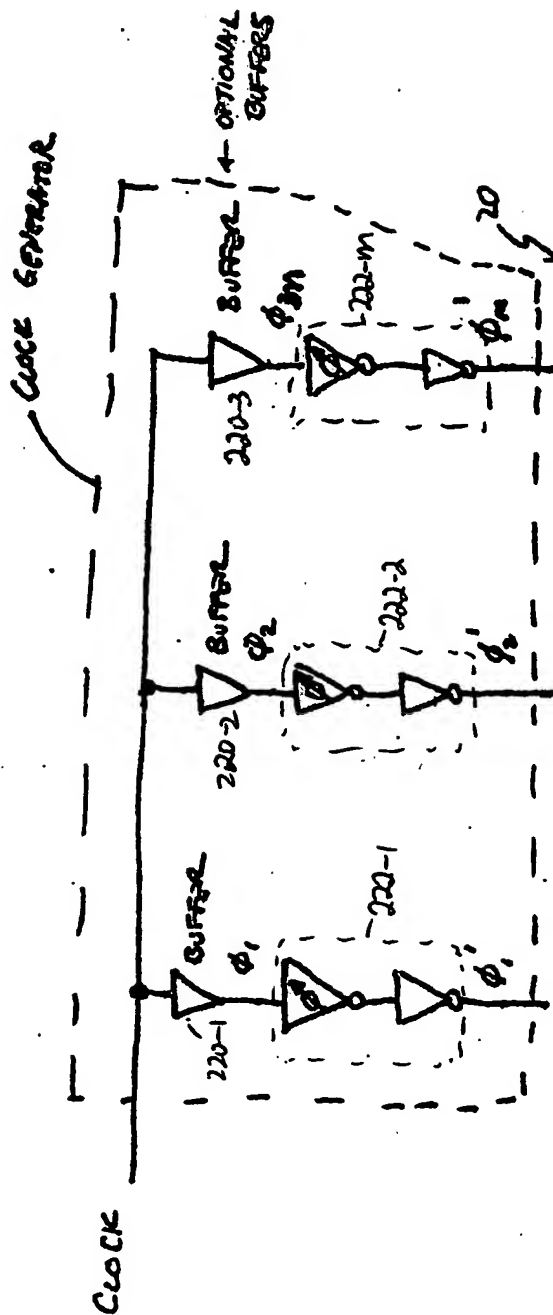


FIG. 9B

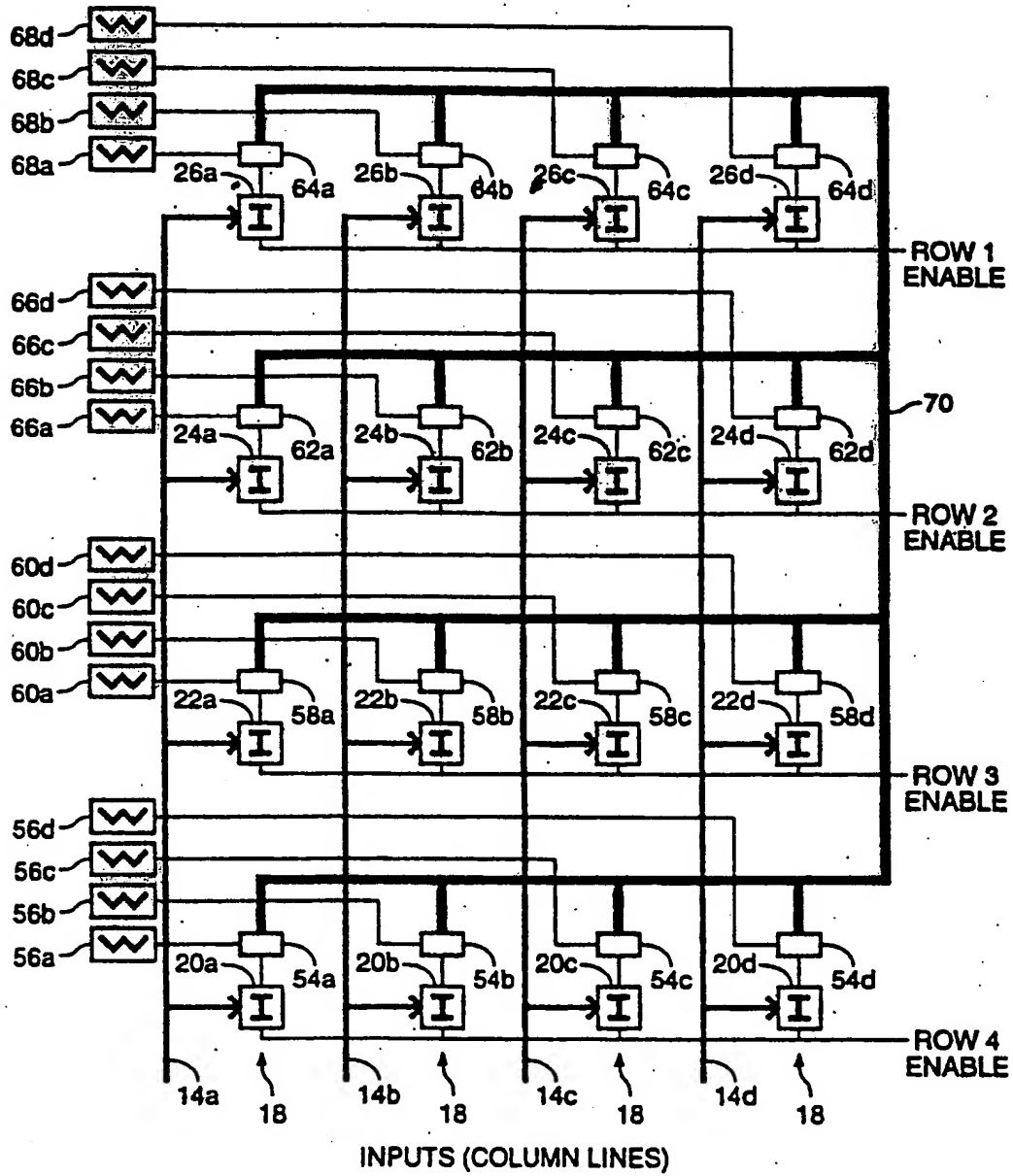


FIG. 10A  
(PRIOR ART)



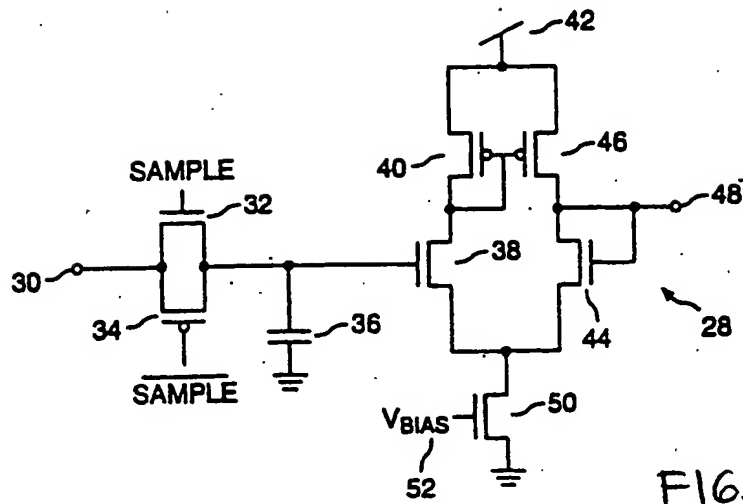
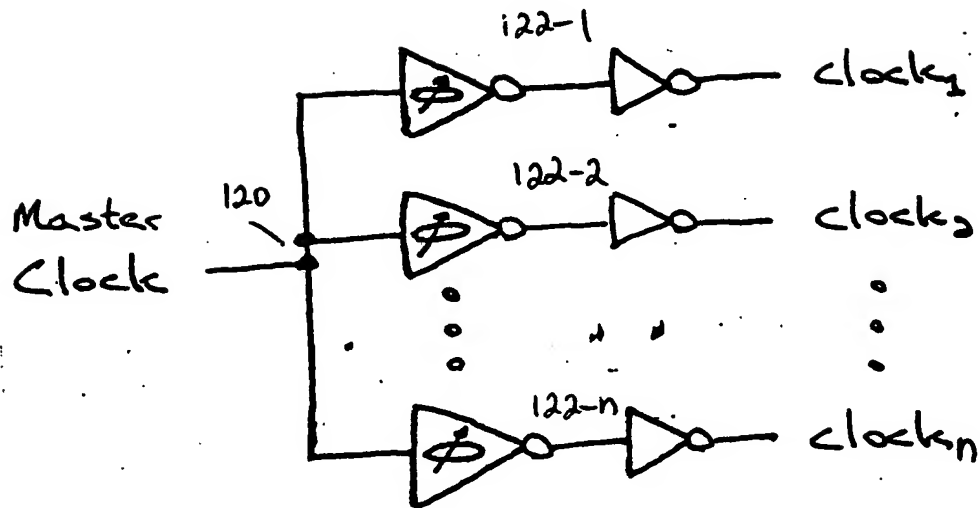


FIG. 10B  
(PRIOR ART)



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FIG. 11A

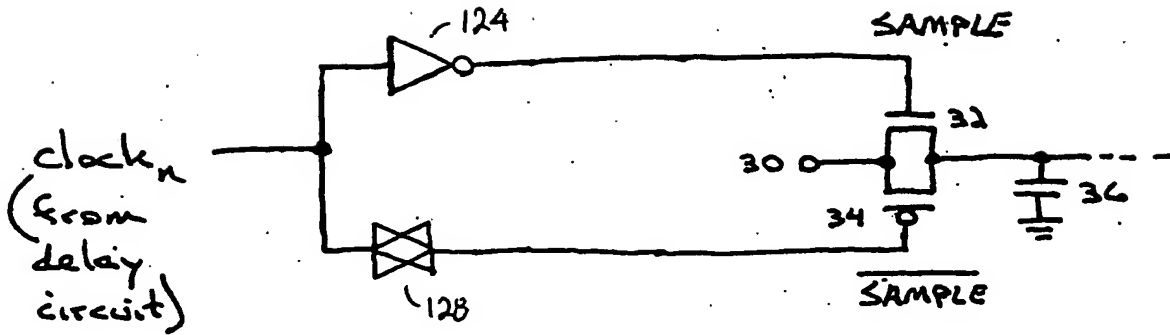


FIG. 11B

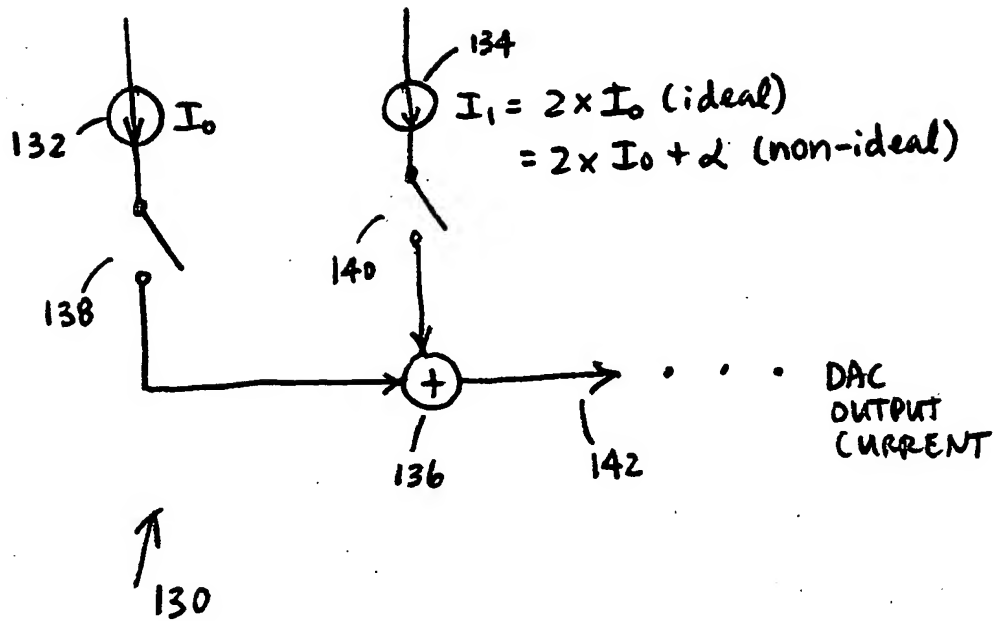


FIG. 12A

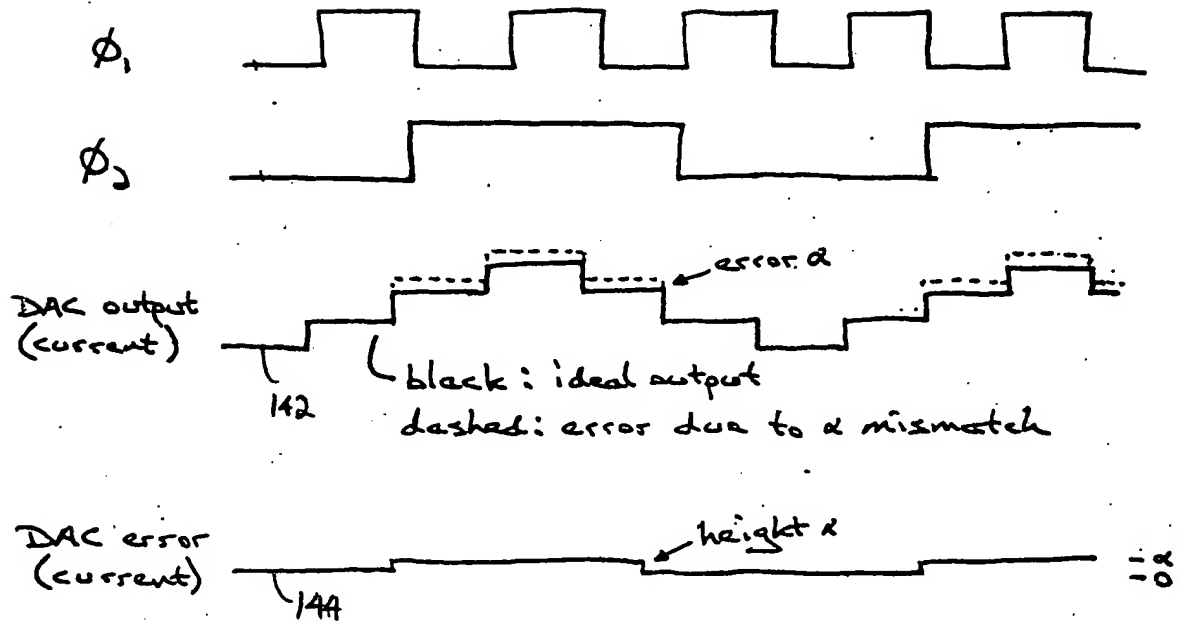


FIG. 12B

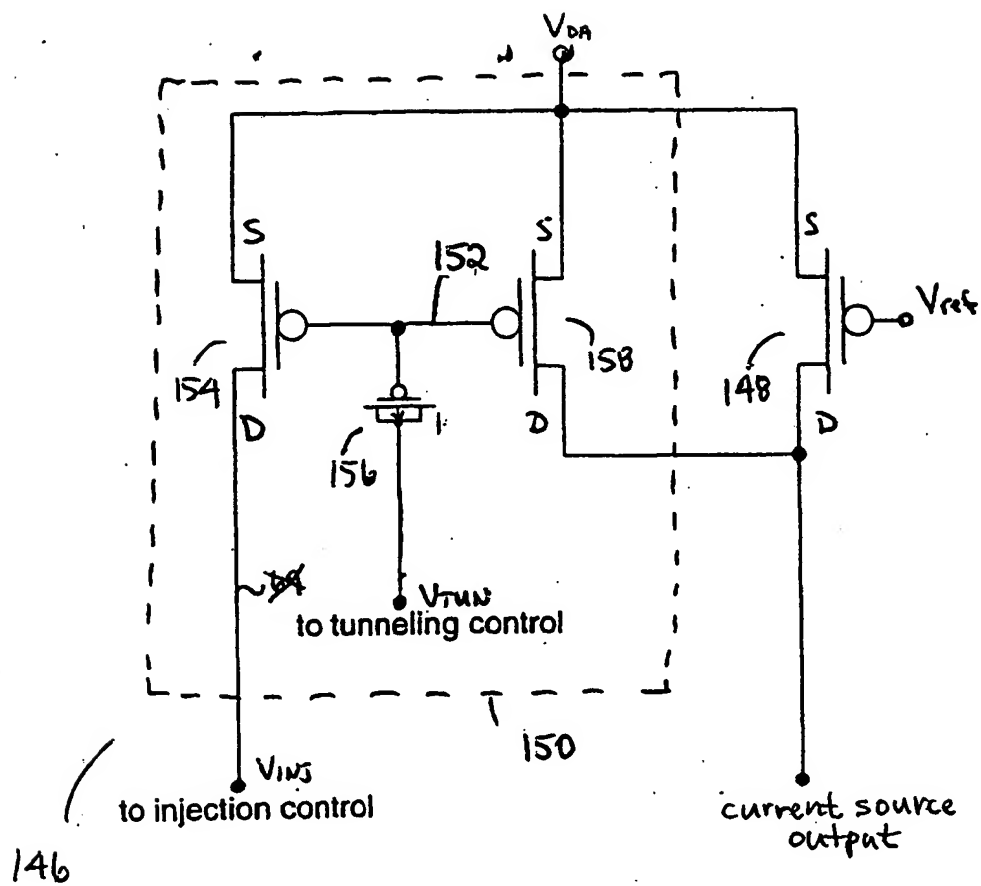


FIG. 13

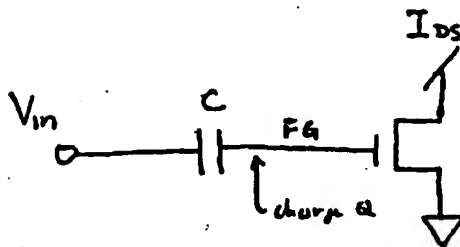


FIG. 14A

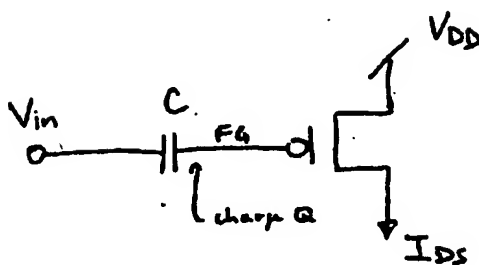


FIG. 14B

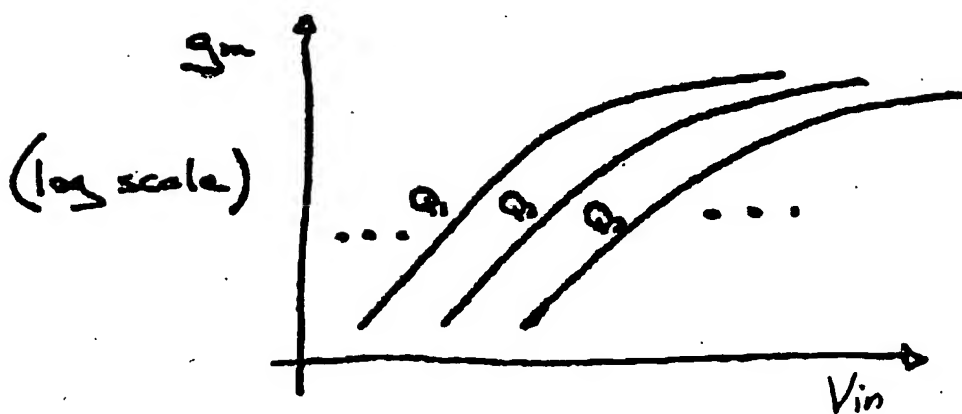


FIG. 14C

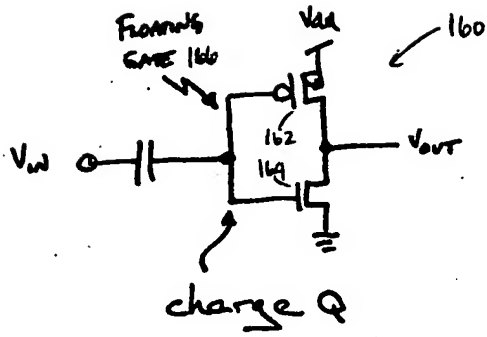


FIG. 15A

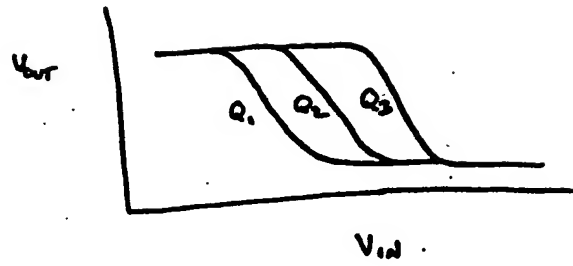


FIG. 15B



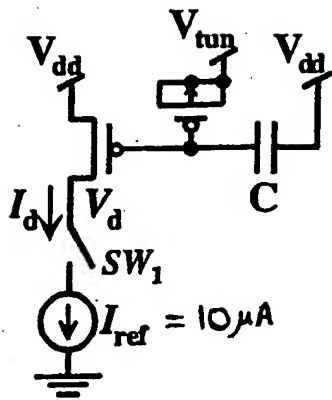


FIG. 16A

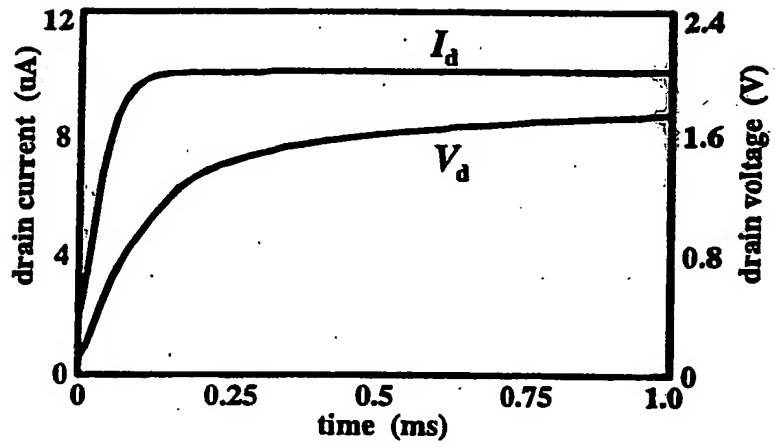
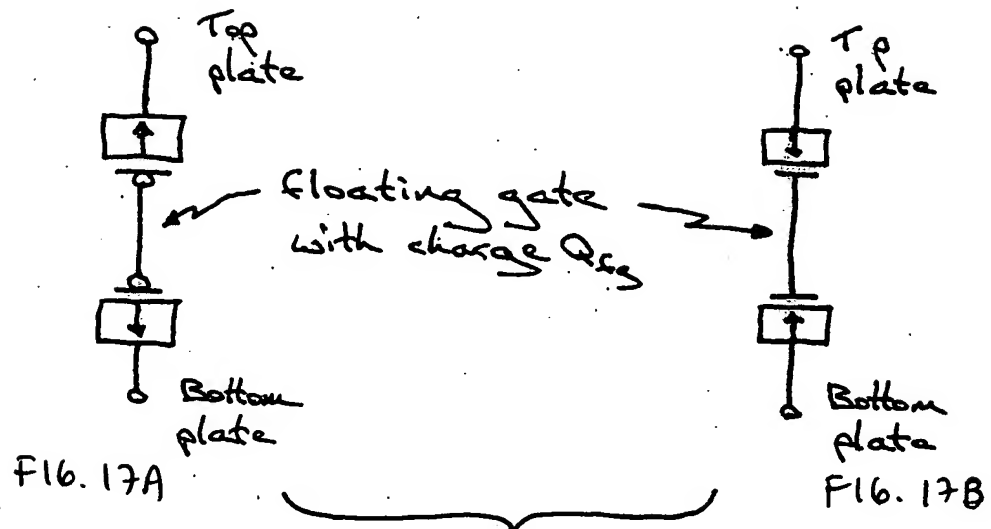
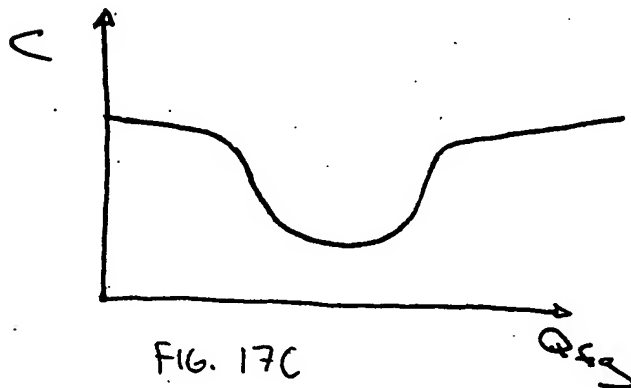


FIG. 16B



equivalent circuit model  $\Rightarrow$



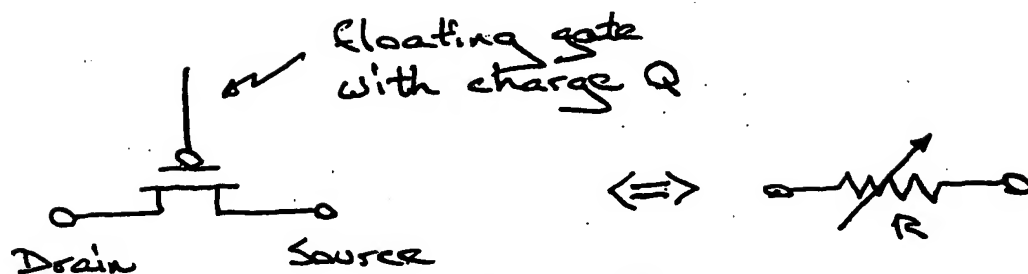


FIG. 18A

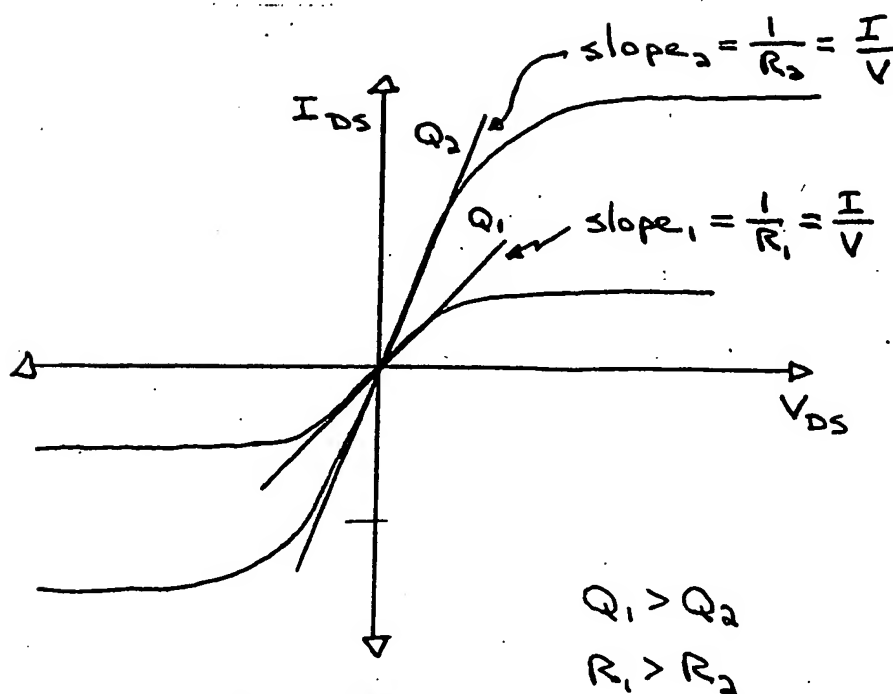


FIG. 18B

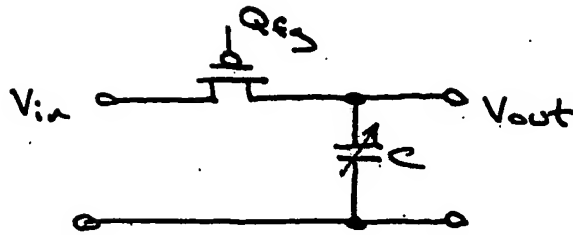


FIG. 19A

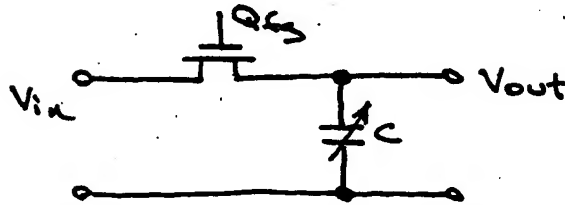


FIG. 19B  $\tau_n = RC$   
( $n=1,2,\dots$ )

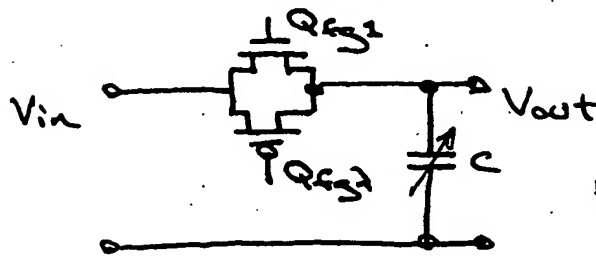


FIG. 19C

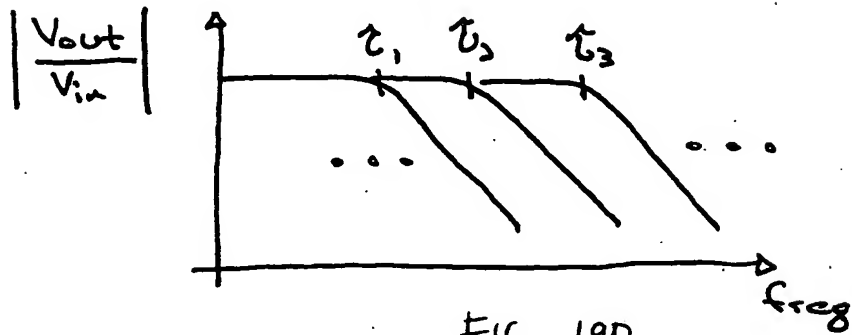


FIG. 19D



FIG. 19E

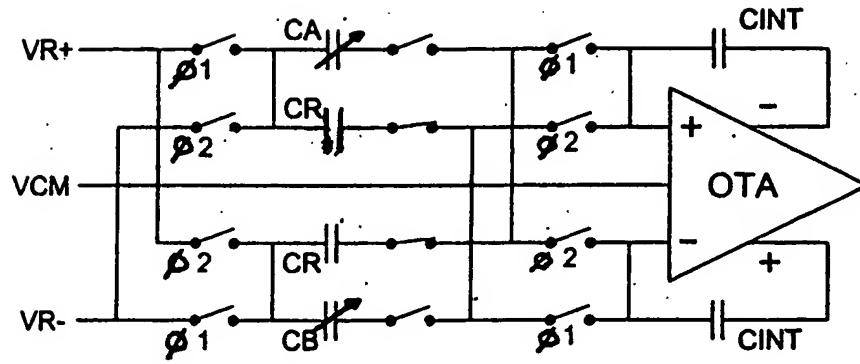


FIG. 20